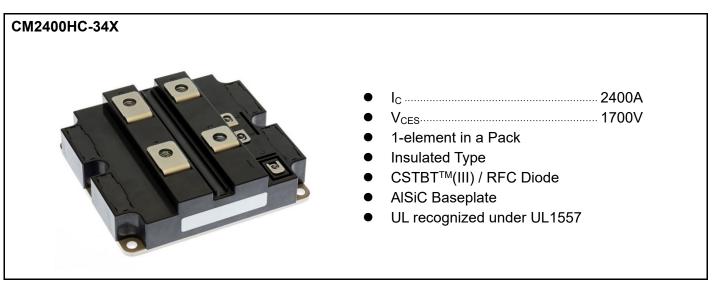


< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

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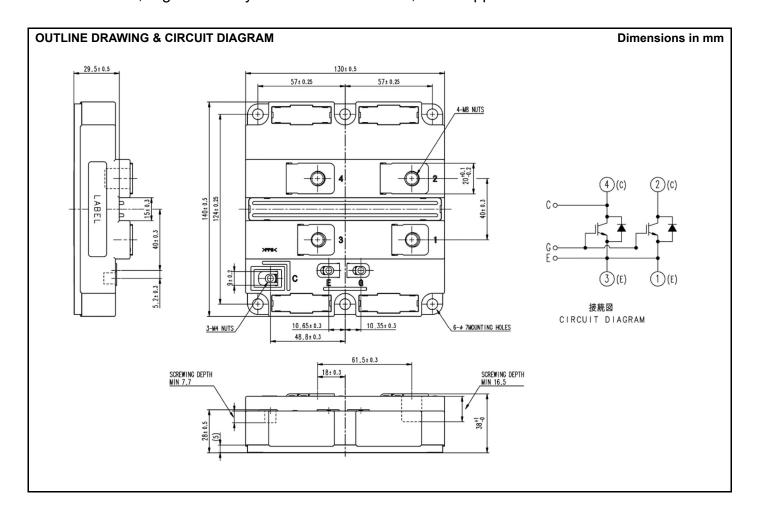
HIGH POWER SWITCHING USE INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



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HIGH POWER SWITCHING USE

INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

MAXIMUM RATINGS

Symbol	Item	Conditions	Ratings	Unit
V _{CES}	Collector emitter valtage	$V_{GE} = 0V, T_j = -40+150$ °C	1700	V
	Collector-emitter voltage	$V_{GE} = 0V, T_j = -50^{\circ}C$	1650	V
V_{GES}	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	±20	V
Ic	Callantan aumant	DC, T _c = 95°C	2400	Α
I _{CRM}	Collector current	Pulse (Note 1)	4800	Α
I _E	Emitter current (Note 2)	DC, T _c = 75°C	2400	Α
I _{ERM}	Emitter current (Note 2)	Pulse (Note 1)	4800	Α
P _{tot}	Maximum power dissipation (Note 3)	T _c = 25°C, IGBT part	13800	W
V _{iso}	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min	6000	V
V _e	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, Q _{PD} ≤ 10pC	2600	V
Tj	Junction temperature		− 50 ~ + 150	°C
T _{jop}	Operating junction temperature		− 50 ~ + 150	°C
T _{stg}	Storage temperature		− 55 ~ + 150	°C
t _{psc}	Short circuit pulse width	$V_{CC} = 1200V, V_{CE} \le V_{CES}, V_{GE} = 15V, T_j = 150^{\circ}C$	6.5	μs

ELECTRICAL CHARACTERISTICS

Cumbal	Itom	Conditions			Limits		
Symbol	Item	Conditions		Min	Тур	Max	Unit
I _{CES}			$T_j = 25^{\circ}C$		_	2.0	
	Collector cutoff current	$V_{CE} = V_{CES}, V_{GE} = 0V$	T _i = 125°C		3.5	—	mA
			$T_j = 150^{\circ}C$		20.0	_	
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10V, I_{C} = 240mA, T_{j} = 25^{\circ}C$		5.5	6.0	6.5	V
I _{GES}	Gate leakage current	$V_{GE} = V_{GES}, V_{CE} = 0V, T_{j} = 25^{\circ}C$		-0.5	_	0.5	μΑ
C _{ies}	Input capacitance	$V_{CE} = 10V, V_{GE} = 0V, f = 100kHz$			817	_	
C _{oes}	Output capacitance	$V_{CE} = 10V, V_{GE} = 0V, T = 100KHZ$ $T_i = 25^{\circ}C$			17.8		nF
C _{res}	Reverse transfer capacitance	1 _j - 25 C			7.2	_	
Q_G	Total gate charge	V_{CC} = 900V, I_C = 2400A, V_{GE} = ±15	5V		51.0	_	μC
		I _C = 2400A (Note 4)	$T_j = 25^{\circ}C$		1.60	_	
V_{CEsat}	Collector-emitter saturation voltage	V _{GE} = 15V	T _j = 125°C		1.85	_	V
		V _{GE} = 13V	T _j = 150°C		1.95	2.45	
t _{d(on)}	Turn-on delay time	V _{CC} = 900V	$T_{j} = 150^{\circ}C$		_	1.50	μs
t _r	Rise time	I _C = 2400A	T _j = 150°C			0.50	μs
	Turn-on switching energy (per pulse) (Note 5)	$V_{GE} = \pm 15V$	T _j = 25°C		0.40	_	J
E _{on(10%)}		$R_{G(on)} = 0.62\Omega$	T _j = 125°C		0.70	_	
		L _S = 75nH	T _j = 150°C		0.75	_	
	Turn-on switching energy (per pulse) (Note 6)	Inductive load	T _j = 25°C		0.50	_	
E _{on}			T _j = 125°C		0.75	_	
		-	T _j = 150°C		0.80	_	
			$T_j = 25^{\circ}C$		6.00	_	
$t_{d(off)}$	Turn-off delay time		T _j = 125°C		6.20	_	μs
			T _j = 150°C		6.35	10.0	
		V _{CC} = 900V	$T_j = 25^{\circ}C$		0.30	_	
t _f	Fall time	I _C = 2400A	T _j = 125°C		0.32	— p	μs
		V _{GE} = ±15V	T _j = 150°C		0.34	1.00	
E _{off(10%)}	Turn-off switching energy (Note 5)	$R_{G(off)} = 5.6\Omega$	T _j = 25°C	_	0.95	_	
		$L_S = 75$ nH	T _j = 125°C		1.10	_	J
	(hei hriise)	Inductive load T	T _j = 150°C	-	1.20	_	
	Turn off quitabing aparau		T _j = 25°C		1.00	_	
E _{off}	Turn-off switching energy		T _j = 125°C	-	1.15	_	J
	(per pulse) (Note 6)	$T_j = 15$		_	1.25	_	

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HIGH POWER SWITCHING USE

INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

ELECTRICAL CHARACTERISTICS

Cumbal	Item		Conditions		Limits			Unit
Symbol					Min	Тур	Max	Unit
			1 0400 A (Note 4)	$T_j = 25^{\circ}C$	_	1.80		
V_{EC}	Emitter-collector voltage	(Note 2)	I _E = 2400A (Note 4)	T _j = 125°C		1.95	_	V
			$V_{GE} = 0V$	T _j = 150°C	_	1.95	2.45	
				T _j = 25°C	l	0.40	_	
t _{rr}	Reverse recovery time	(Note 2)		T _j = 125°C		0.55	_	μs
				T _j = 150°C	1	0.60	_	
				T _j = 25°C	1	1790	_	
Irr	Reverse recovery current	(Note 2)		T _j = 125°C		1930	_	Α
				T _j = 150°C	_	1980	_	
			V _{CC} = 900V	T _j = 25°C	_	430	_	
Q _{rr(10%)}	Reverse recovery charge	(Note 2,7)	I _E = 2400A	T _j = 125°C	l	720	_	μC
			$V_{GE} = \pm 15V$	T _j = 150°C		820	_	
			$R_{G(on)} = 0.62\Omega$	T _j = 25°C	_	480	_	
Q_{rr}	Reverse recovery charge	(Note 2,6)	L _S = 75nH	T _j = 125°C	l	785	_	μC
	, ,	Inductive load	T _j = 150°C	l	890	_		
	D			T _j = 25°C		0.22	_	
E _{rec(10%)}	Reverse recovery energy	(Note 2,5)		T _j = 125°C	_	0.40	_	J
	(per pulse) (Note 2,5)		T _j = 150°C	1	0.46	_		
	D]	T _j = 25°C	1	0.25	_	
E _{rec}	Reverse recovery energy	(Note 2,6)		T _j = 125°C	1	0.45	_	J
	(per pulse) (Note 2,6)		T _i = 150°C	_	0.55	_		

THERMAL CHARACTERISTICS

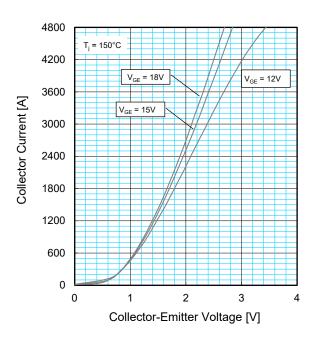
Cumbal	Item	Conditions		Limits		
Symbol				Тур	Max	Unit
$R_{th(j-c)Q}$	Thermal resistance	Junction to Case, IGBT part	-		9.0	K/kW
$R_{th(j-c)D}$	mermai resistance	Junction to Case, FWDi part			12.5	K/kW
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, λ_{grease} = 1W/m K, $D_{(c-s)}$ = 80 μ m	_	7.5	_	K/kW

MECHANICAL CHARACTERISTICS

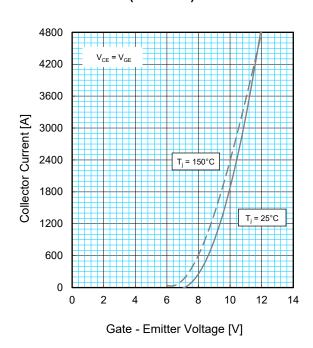
Symbol	Item	Conditions	Limits			1.1 14
		Conditions	Min	Тур	Max	Unit
Mt	Mounting torque	M8 : Main terminals screw	7.0	_	19.0	N⋅m
M _s	Mounting torque	M6 : Mounting screw	3.0	_	6.0	N⋅m
M _t	Mounting torque ^(Note 8)	rque ^(Note 8) M4 : Auxiliary terminals screw		_	3.0	N·m
m	Mass		_	0.9	_	kg
CTI	Comparative tracking index		600	_	_	_
d _a	Clearance		19.5	_	-	mm
d _s	Creepage distance		32.0	_	-	mm
L _{P CE}	Parasitic stray inductance		— 12.0 —		nΗ	
R _{CC'+EE'}	Internal lead resistance	T _C = 25°C	_	0.14	_	mΩ

- Note1. Pulse width and repetition rate should be such that junction temperature (T_j) does not exceed T_{jopmax} rating.
- Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD_i).
- Note3. Junction temperature (T_j) should not exceed T_{jmax} rating (150°C).
- Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise.
- Note5. The integration range of switching energies is from 10%V_{CE} to 10%I_C(10%I_E).
- Note6. Definition of all items is according to IEC 60747, unless otherwise specified.
- Note7. The integration range of reverse recovery charge is from I_E = 0A to 10% I_E .
- Note8. The maximum specified value is under the condition of using PCB mounted on the power module. In case no PCB is used this maximum torque for M4 screw is 1.9 Nm.

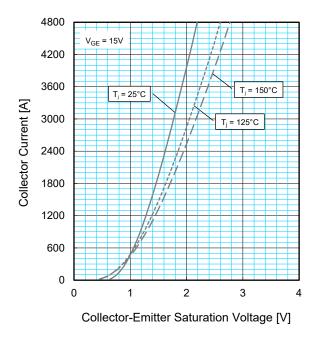
OUTPUT CHARACTERISTICS (TYPICAL)



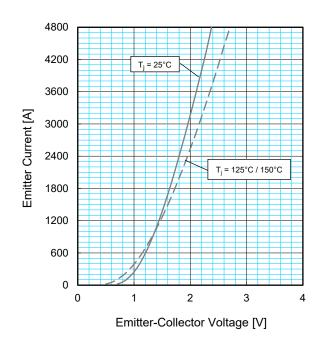
TRANSFER CHARACTERISTICS (TYPICAL)



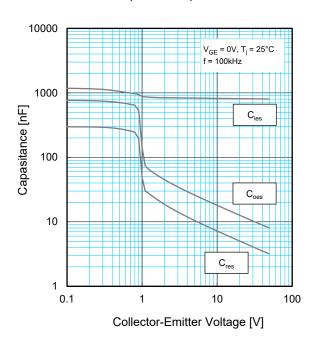
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



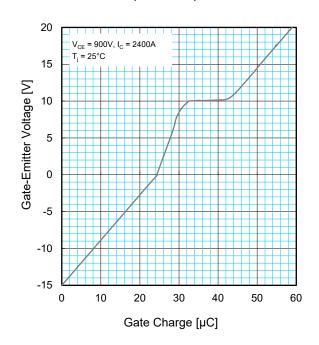
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



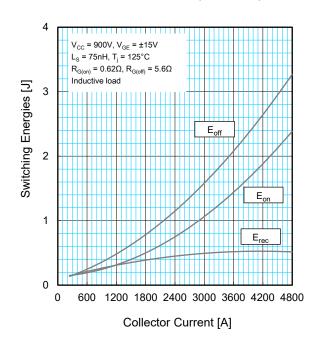
CAPACITANCE CHARACTERISTICS (TYPICAL)



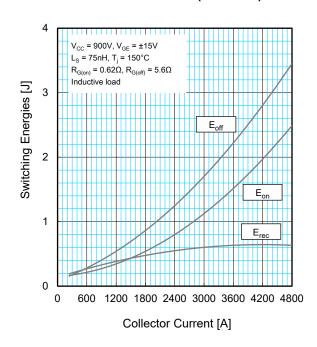
GATE CHARGE CHARACTERISTICS (TYPICAL)



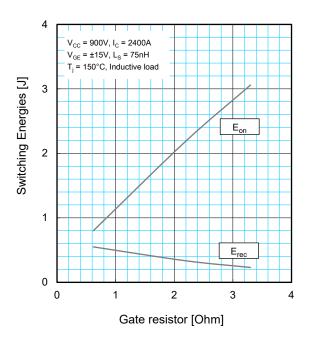
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



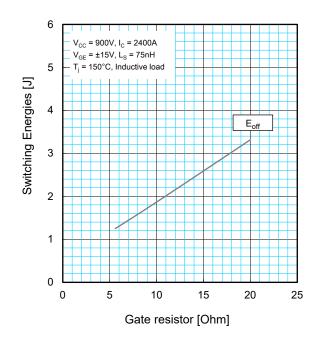
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



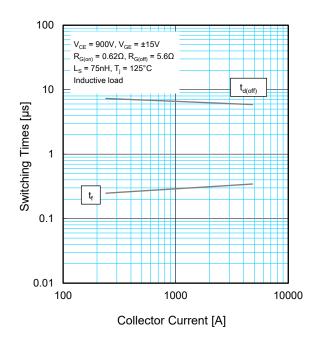
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



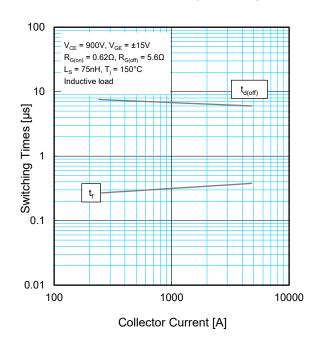
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



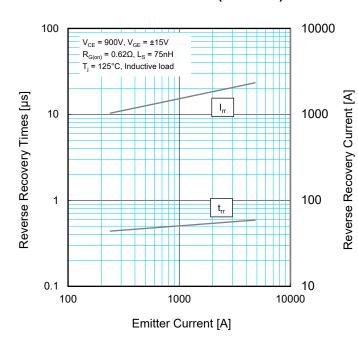
HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



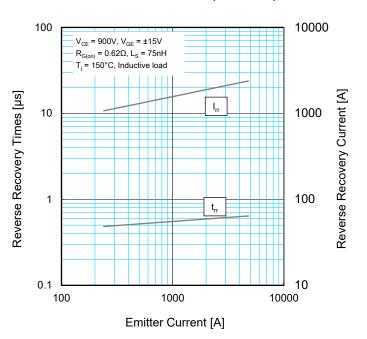
HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



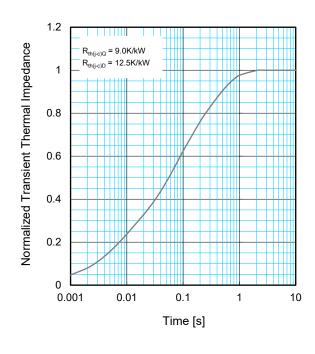
FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



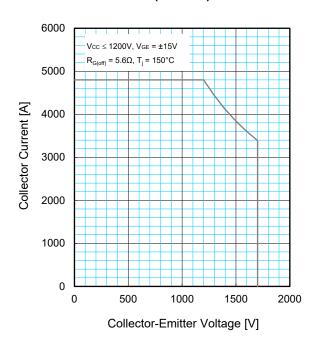
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



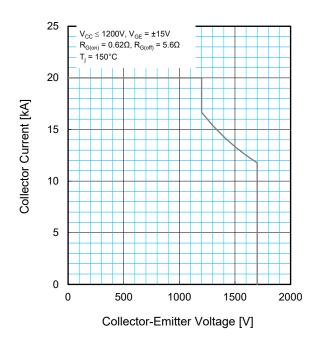
$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - \exp\left(-\frac{t}{\tau_{i}}\right) \right\}$$

		1	2	3	4
R_{i}	/ R _{th(j-c)} :	0.0096	0.1893	0.4044	0.3967
7	t i [sec]:	0.0001	0.0058	0.0602	0.3512

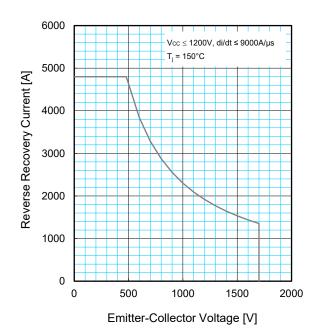
REVERSE BIAS SAFE OPERATING AREA (RBSOA)



SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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HIGH POWER SWITCHING USE

INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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